Patent Abstracts of Japan

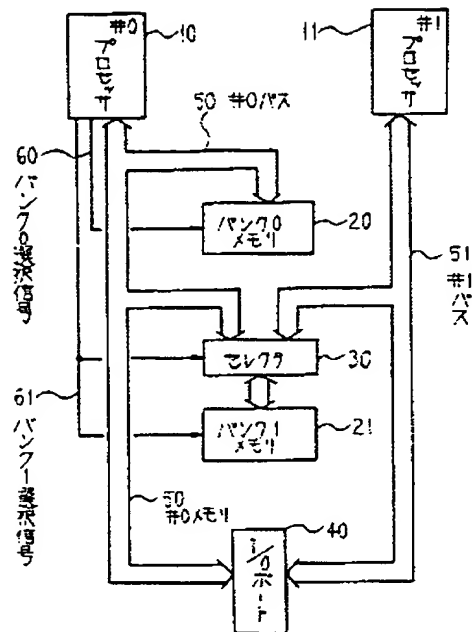|		
PUBLICATION NUMBER	:	03105440
PUBLICATION DATE	:	02-05-91
APPLICATION DATE	:	19-09-89
APPLICATION NUMBER	:	01244211

APPLICANT : NEC CORP;

INVENTOR : YOKOTA KEIICHI;

INT.CL. : G06F 12/06

TITLE : SHARED MEMORY ACCESS SYSTEM

ABSTRACT : PURPOSE: To economically realize the shared memory of a multiprocessor system so as to improve reliability by connecting a bank which a bank memory does not use as the memory of the other processor.

CONSTITUTION: A #0 processor 10 is always used and it can be connected to a bank 0 memory 20 through a #0 bus 50. It is connected to a bank 1 memory 21 by the selection of a selector 30. A #1 processor 11 is connected only to the bank 1 memory 21 by the selection of the selector 30 through a #1 bus 51. The selector 30 always and selectively connects the #1 bus 51 to the bank 1 memory 21, and connects the #0 bus 50 to the bank 1 memory 21 only when it receives a bank 1 selection signal from the #0 processor 10. An I/O port 40 receives information from respective #0 and #1 processors 10 and 11 and stores the number of a memory bank in the middle of use. Consequently, the #1 processor 11 uses the bank 1 memory 21 only when the port 40 is in a use permission state.